Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (01-09)
formation Disclosure Statement (IDS) Filed

Approved for use through 02/28/2009, OMB 0551-0031

U.S. Patent and Trademank Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(Not for submission under 37 CFR 1.99)

	10002217		
	2001-11-01		
Craig	Nemecek		
	2123		
Jason	n Scott Proctor		
oer	CD01207M		
		2001-11-01 Craig Nemecek 2123 Jason Scott Proctor	

U.S.PATENTS										
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date		Name of Pate of cited Docu	entee or Applicant ment	Relev	s,Columns,Lines where vant Passages or Relev es Appear	
	1								,	
If you wis	h to ac	dd additional U.S. Pater	t citatio	n inform	ation pl	ease click the	Add button.			
			U.S.P	ATENT.	APPLI	CATION PUBL	ICATIONS			
Examiner Initial*	Cite No	Publication Number	Kind Code ¹			Name of Pate of cited Docu	entee or Applicant ment	Relev	s,Columns,Lines where vant Passages or Relev es Appear	
	1						1			
If you wis	h to a	d additional U.S. Publi	shed Ap	plication	citatio	n information p	lease click the Ad	d butto	on.	
				FOREIC	SN PA1	ENT DOCUM	ENTS			
Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ² i		Kind Code4	Publication Date	Name of Patente Applicant of cited Document		Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T5
	1									
If you wis	h to a	dd additional Foreign Pa	atent Do	cument	citation	information pl	ease click the Add	buttor	1 .	
			NON	I-PATEN	IT LITE	RATURE DO	CUMENTS			
Examiner Initials*	Examiner Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), authistor, city, and/or onjustable of the control of t								T 5	

(Not for submission under 37 CFR 1.99)

Application Number		10002217		
Filing Date		2001-11-01		
First Named Inventor Craig		Nemecek		
Art Unit		2123		
Examiner Name	Jason	n Scott Proctor		
Attorney Docket Number		CD01207M		

/JP/	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	
/JP/	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm; 2 pages printed on 4/12/2005	
/JP/	3	ANONYMOUS, "F/Port:Fast Parallel Port for the PC" installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm; 25 pages	
/JP/	4	Nam et al.; "Fast Development of Source-Level Debugging System Using Hardware Emulation"; IEEE 2000; 4 pages	
/JP/	5	Huang et al.; "Iceberg: An Embedded In-Cicuit Emulator Synthesizer For Microcontrollers"; ACM 1999; 6 pages	
/JP/	6	Khan et al., "FPGA Architectures for Asic Hardware Emulators"; IEEE 1993; 5 pages	·
/JP/	7	Oh et al.; Emulator Environment Based on an FPGA Prototyping Board*; IEEE 21-23; June 2000; 6 pages	
/JP/	8	Hong et al.; "An FPGA-Based Hardware Emulator for Fast Fault Emulation"; IEEE 1997; 4 pages	
/JP/	9	Ching et al.; "An In-Curcuit-Emulator for TMS320C25", IEEE 1994; 6 pages	
/JP/	10	Pasternak; "In-Circuit-Emulation in ASIC Architecture Cor Designs", IEEE 1989; 4 pages	
/JP/	11	Melear, "Using Background Modes for Testing, Debugging and Emulation of Microcontrollers", IEEE 1997; 8 pages	

(Not for submission under 37 CFR 1.99)

Application Number	10002217
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Numb	er CD01207M

/JP/	12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	
/JP/	13	Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface"; August 1999; Embedded Performance, Inc.; 3 pages	
/JP/	14	Sedory, "A Guide to Debug"; 2004; retrieved on May 20, 2005; 7 pages	
/JP/	15	"Microsoft Files Summary Judgement Molions"; February 1999; Microsoft PressPass; retrieved on May 20, 2005 from http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp; 3 pages	
/JP/	16	Xerox; "Mesa Debugger Documentation", April 1979; Xerox Systems Development Department; Version 5.0; 33 pages	
/JP/	17	Stallman et al.; "Debugging with the GNU Source-Level Debugger", January 1994; retrieved on May 2, 2005 from http://www.cs.ulah.edu; 4 pages	
/JP/	18	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on 1000000000000000000000000000000000000	
/JP/	19	Stan Augarten; "The Chip Collection - Introduction - Smithsonian Institute"; "State of the Art"; "The First 256-Bit Static RAM"; retrieved on November 14, 2005 from http://smithsonianchips.si.edu/augarten/p24.htm; 2 pages	
/JP/	20	"POD - Piece of Data, Plain Old Documentation, Plain Old Dos"; retrieved on November 14, 2005 from http://www.auditmypc.com/acronym/POD.asp; 2 pages	
/JP/	21	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2003 from http://en.wikipedia.org/wiki/Pod; 3 pages 11/8/2007	
/JP/	22	"pod-definition by dict die.net"; retrieved on November 14, 2005 from http://dict.die.net/pod: 2 pages 11/8/2007	

(Not for submission under 37 CFR 1.99)

Application Number		10002217		
iling Date		2001-11-01		
irst Named Inventor	Craig	Nemecek		
Art Unit		2123		
xaminer Name	Jason	Scott Proctor		
Attorney Docket Number		CD01207M		

/JP/	23	"In-Curcuit Emulators - descriptions of the major ICEs around"; retrieved on N ovember 11, 2006 from http://www. algonet.se/~staffann/developer/emulator.htm, 6 pages 11/8/2007	
/JP/	24	CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation;" October 10,2001; 09/975,104; Snyder; 35 pages	
/JP/	25	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	
/JP/	26	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools", Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	
/JP/	27	Microsoft Press Computer User's Dictionary; 1998; 3 pages multiple-user system to multisync monitor	
/JP/	28	CD00188; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	
/JP/	29	CD00185; "Emulator Chip-Board Architecture for Interface;" October 10, 2001; 09/975,030; Snyder et al.; 37 pages	
/JP/	30	Wikipedia - Main Page, retrieved on March 8, 2006 from http://en.wikipedia.org/wiki/Main_Page and http://en.wikipedia.org/wiki/Wikipedia.lntroduction; 5 pages	
/JP/	31	Wikipedia - Processor register, retrieved on March 7, 2006 from http://en.wikipedia.org/wiki/Processor_register, 4 pages	
/JP/	32	Dahl, et al.; "Emulation of the Sparde Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE; 9 pages	
/JP/	33	Bauer et at; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation", 1998; Design Automation Conference Proceedings; 8 pages	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Filing Date	
First Named Inventor	Craig
Art Unit	

Application Number		10002217	
Filing Date		2001-11-01	
First Named Inventor Craig		Nemecek	_
Art Unit		2123	_
Examiner Name Jason		n Scott Proctor	_
Attorney Docket Number		CD01207M	_

/JP/	34	Jonathan B. Rosenburg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 250 pages 264 pages	
/JP/	35	USPTO Final Rejection for Application Number 10/001,478 (CD01213M) dated 04/20/2009; 16 pages	
/JP/	36	USPTO Notice of Allowance for Application Number 09/994,600 (CD01174M) dated 04/03/2009; 5 pages	
/JP/	37	USPTO Notice of Allowance for Application Number 09/989,777 (CD01208M) dated 03/09/2009; 7 pages	_ D
/JP/	38	USPTO Notice of Allowance for Application Number 09/992,076 (CD01210M) dated 02/27/2009; 6 pages	
/JP/	39	USPTO Non-Final Rejection for Application Number 09/989,771 (CD01182) dated 04/30/2009; 11 pages	
/JP/	40	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 03/25/2009; 7 pages	
/JP/	41	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 10/24/2008; 7 pages	
/JP/	42	USPTO Non-Final Rejection for Application Number 09/989,762 (CD01175M) dated 07/27/2005; 15 pages	
/JP/	43	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 06/02/2008; 6 pages	
/JP/	44	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 01/02/2008; 6 pages	

(Not for submission under 37 CFR 1.99)

Application Number		10002217	
Filing Date		2001-11-01	
irst Named Inventor	Craig Nemecek		
Art Unit		2123	
Examiner Name	Jason Scott Proctor		
Attorney Docket Number		CD01207M	

/JP/	45	USPTO Non-Final Rejection for Application Number 09/989,762 (CD01	175M) dated 07/23/2007; 15 pages				
/JP/	46	USPTO Final Rejection for Application Number 09/989,762 (CD01175M) dated 01/26/2007; 13 pages					
/JP/	47	USPTO Non-Final Rejection for Application Number 09/989,762 (CD01175M) dated 08/10/2006; 18 pages					
/JP/	48	USPTO Final Rejection for Application Number 09/989,762 (CD01175M) dated 03/14/2006; 19 pages					
If you wish to add additional non-patent literature document citation information please click the Add button							
EXAMINER SIGNATURE							
Examiner Signature /Jason Proctor/		Date Considered	05/18/2009				

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

See Kind Codes of USPTO Patent Documents at www.USPTO.GOV/cm/MPEP901.04. 2 Enter office that issued the document, by the two-letter code (WIPO Standard ST3.) Per Japanese patent documents, be indication of the year of the respin of the Emperor must precede the serial number of the patent document.
*Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ² Applicant is to place a check mark here if English language translation is attached.